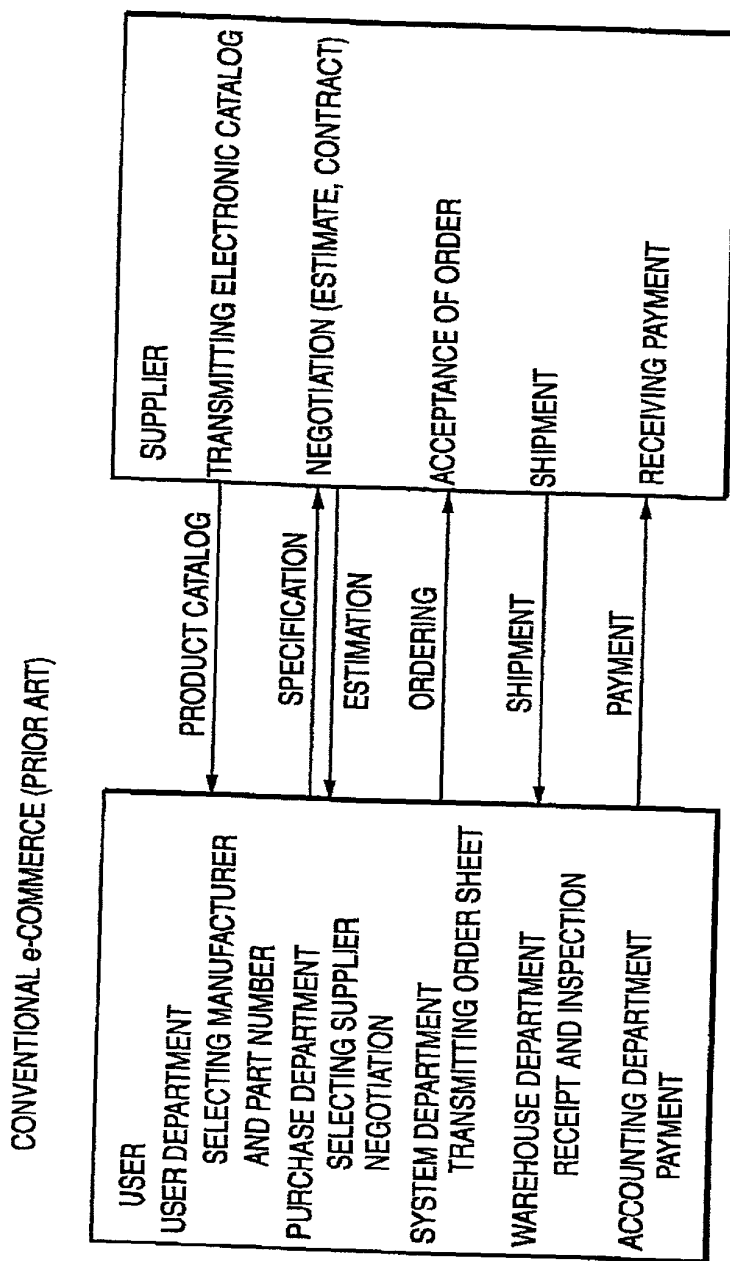
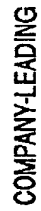


FIG.1

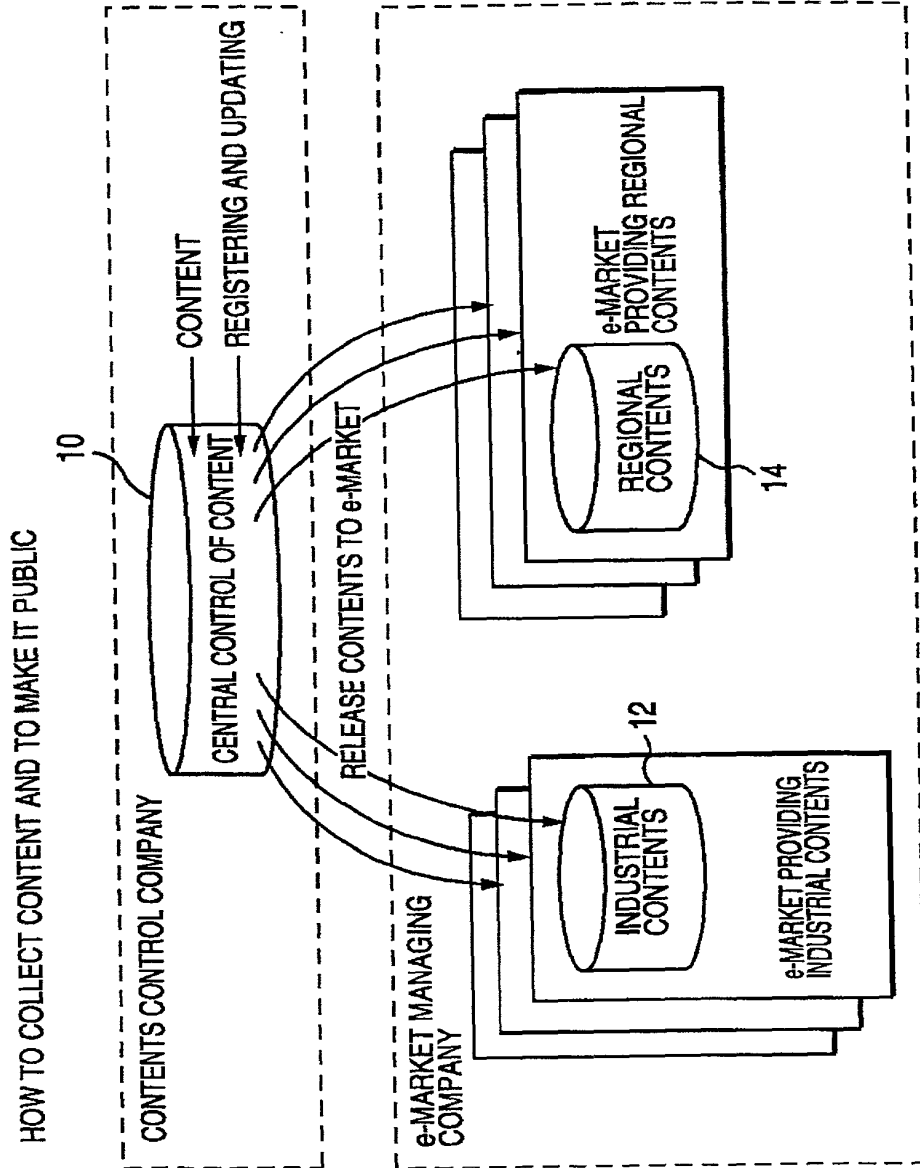


INTERNET BUSINESS (PRIOR ART)



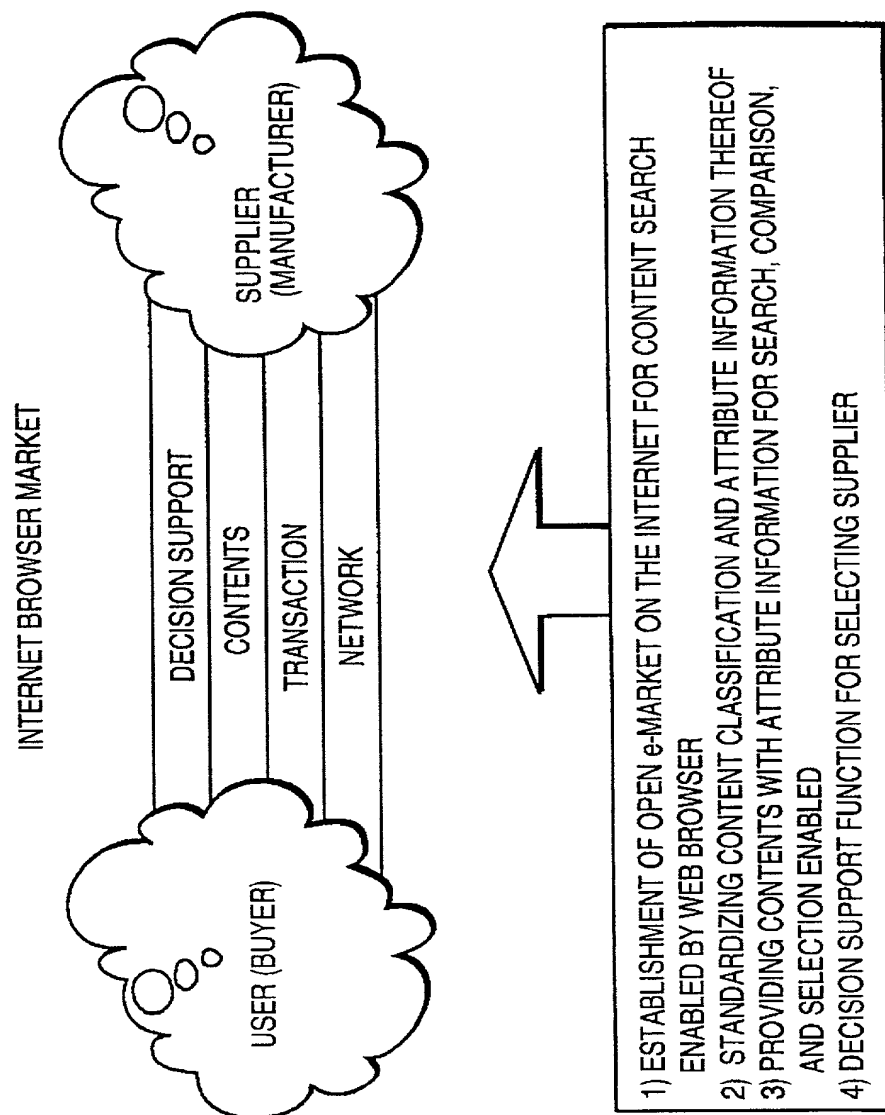
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FIG. 3



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FIG. 4



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FIG. 5

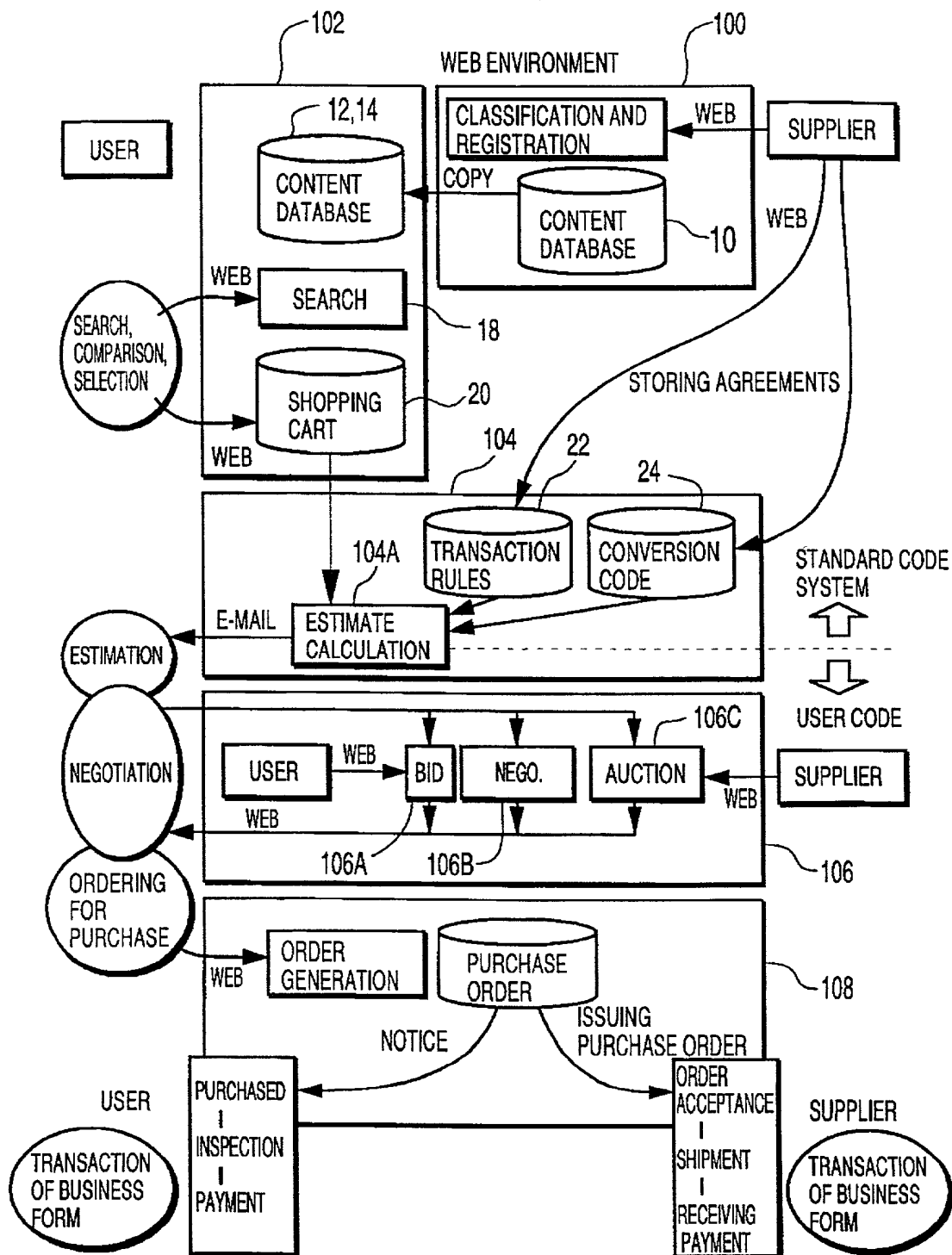
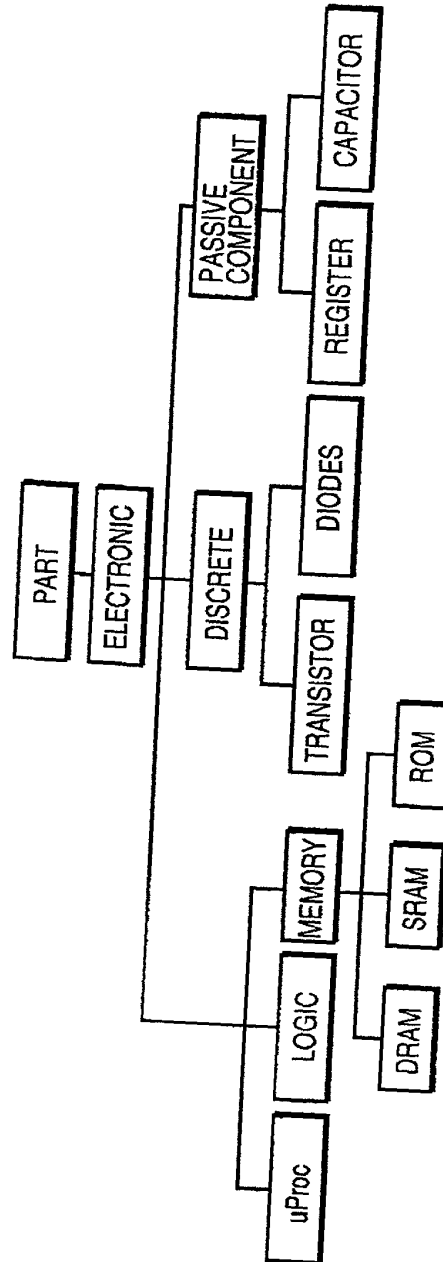


FIG. 6

SCS CLASSIFICATION SYSTEM



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FIG. 7A

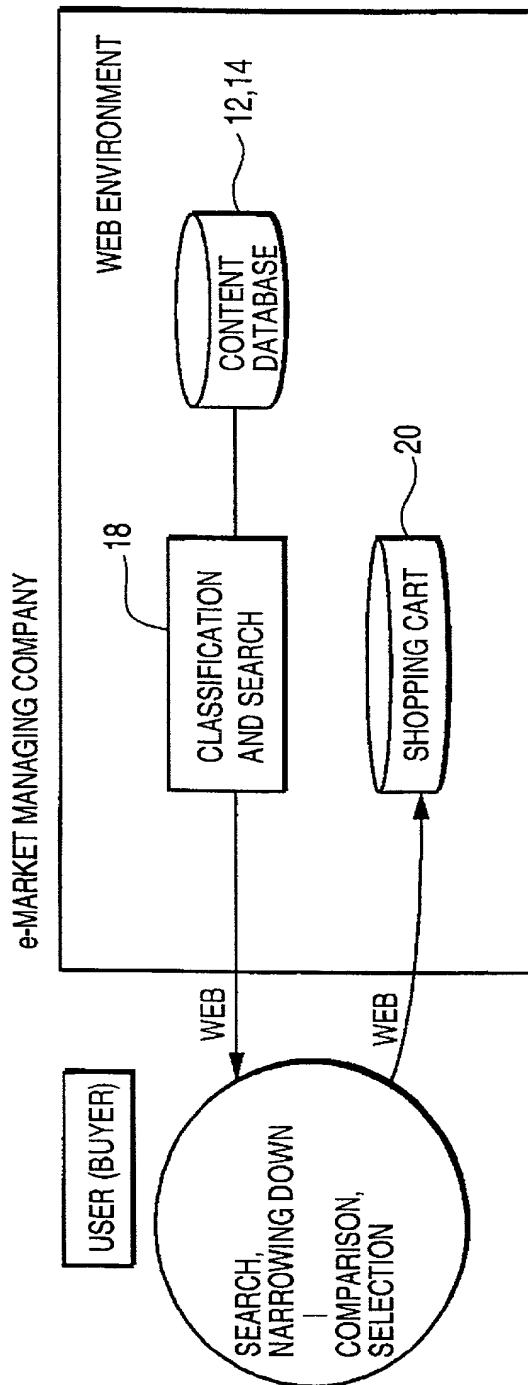


FIG. 7B

A

☐ Standard Ball Bearings

- ☐ Business Supplies
- ☐ Operational Supplies
 - ☐ Pipes, Valves and Fittings
 - ☐ Bearings
 - ☐ Ball Bearings
 - ☐ Standard Ball Bearings
 - ☐ Ball Thrust Bearings
 - ☐ Double Row Bearings
 - ☐ Super Precision Bearings
 - ☐ Roller Bearings
 - ☐ Linear Bearings
 - ☐ Spherical Bearings
 - ☐ Needle Bearings
 - ☐ Plastic Bearings
 - ☐ Bronze Bearings
 - ☐ Motors
 - ☐ Electrical Supplies
 - ☐ Mill Supplies
 - ☐ Safety Supplies

B

Standard Ball Bearings

Name	Supplier Part Number	Description	Inside Diameter	Load Capacity
Motion Industries				
			10 : 20	mm
				kg

C

☐ Results: Standard Ball Bearings

Supplier Part Number	Description
300-2235-00	BRG, Ball Sealed Deep - Groove Metric, 45mm x 72mm x 16mm
300-4495-00	BRG, Ball Sealed Deep - Groove Metric, 40mm x 80mm x 16mm
300-1330-00	BRG, Ball Sealed Deep - Groove Metric, 45mm x 85mm x 16mm
300-0189-00	BRG, Ball Deep - Groove Metric, 50mm x 90mm x 20mm
300-8023-00	BRG, Ball Deep - Groove Metric, 30mm x 62mm x 16mm
300-8822-00	BRG, Ball Deep - Groove Metric, 15mm x 35mm x 11mm

Count Page 1

☐ Results:Sta...

☐ Dashboard ☐ Class Bro... ☐ Search:Sta...

Page: 1/1 Row: 1/25

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FIG. 8

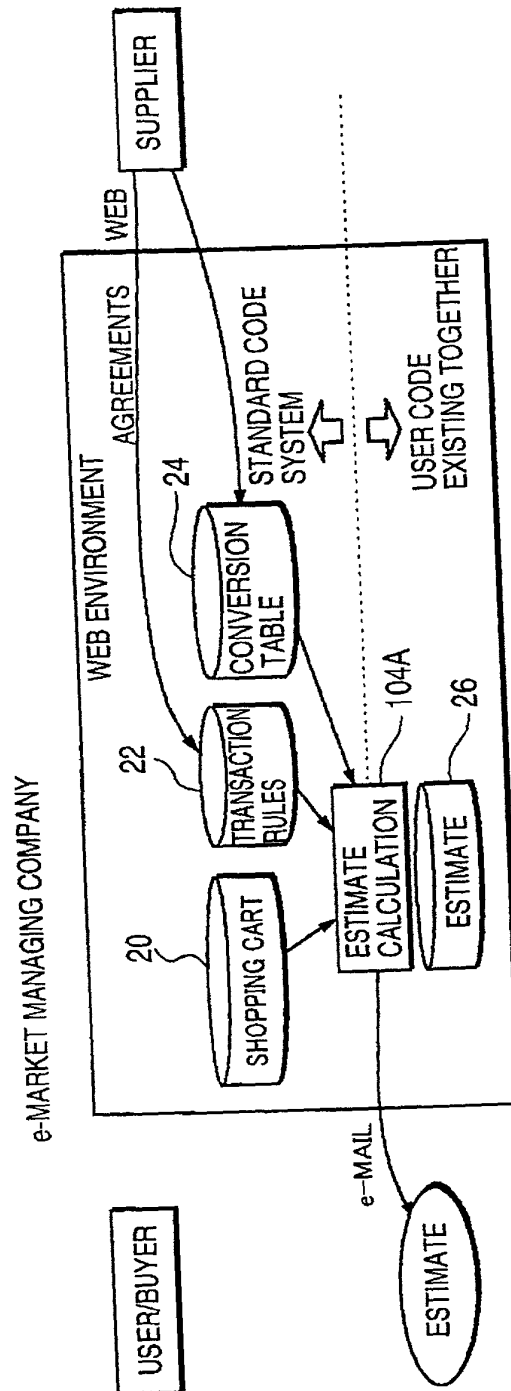
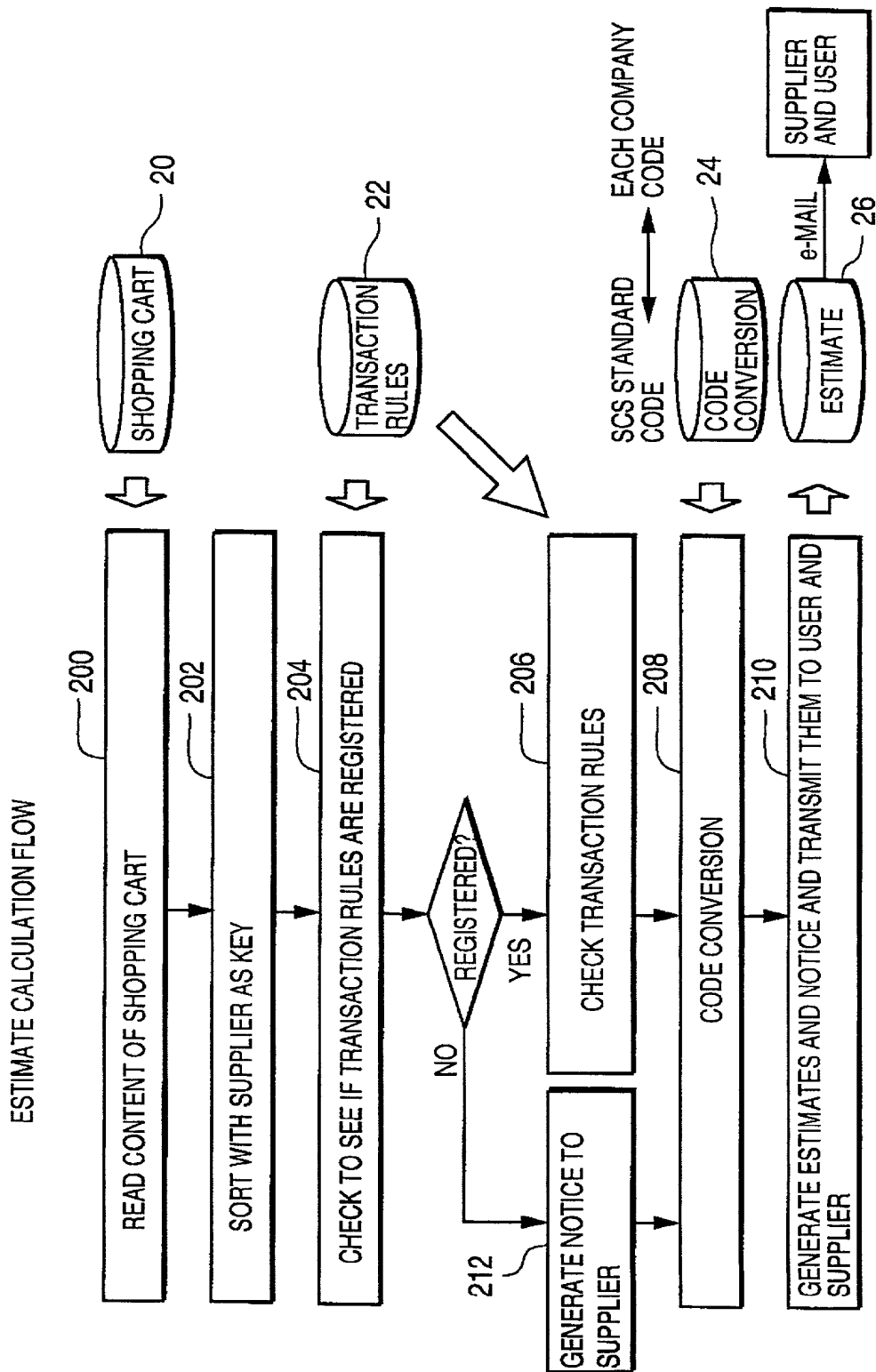


FIG. 9



PURCHASE CONDITION SETTLEMENT FLOW

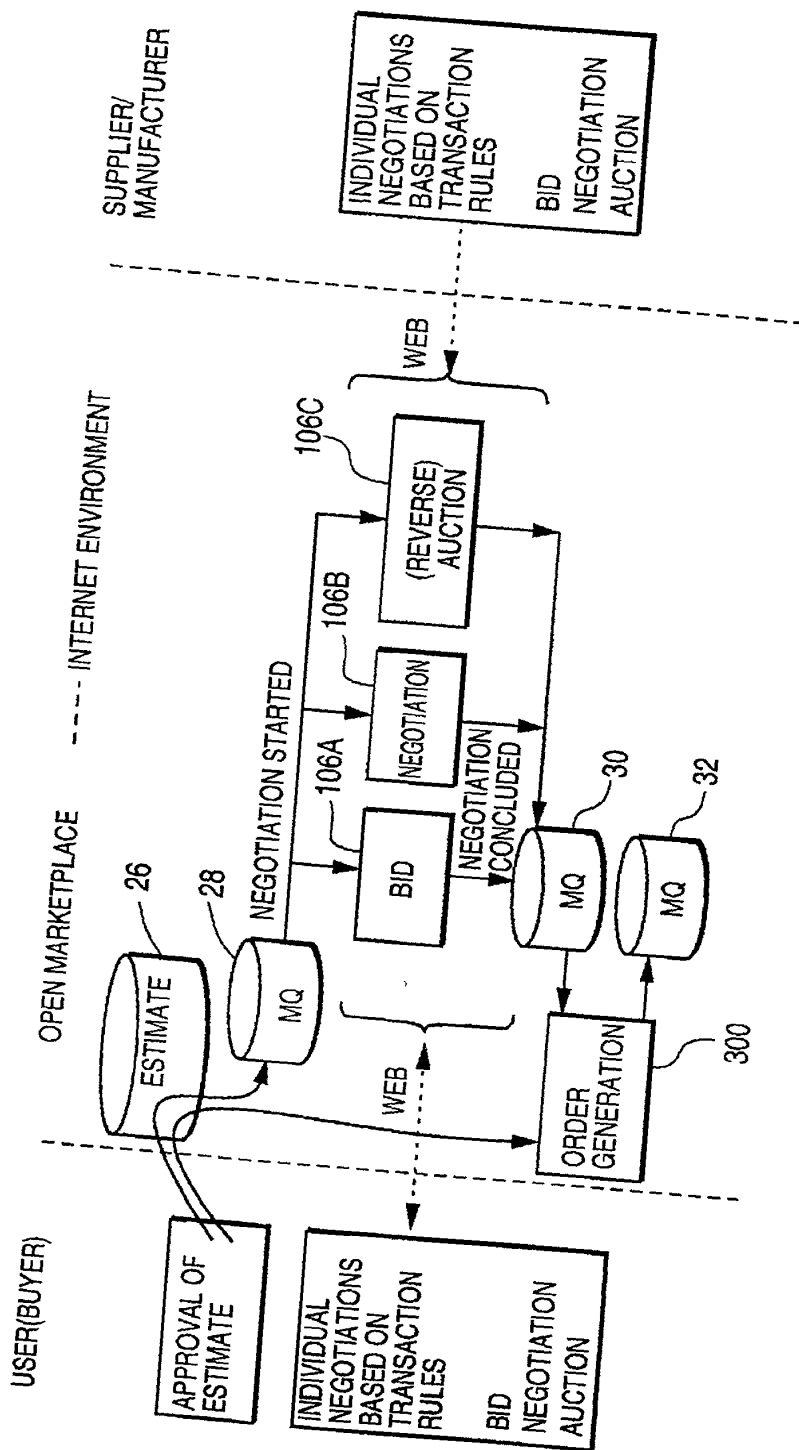
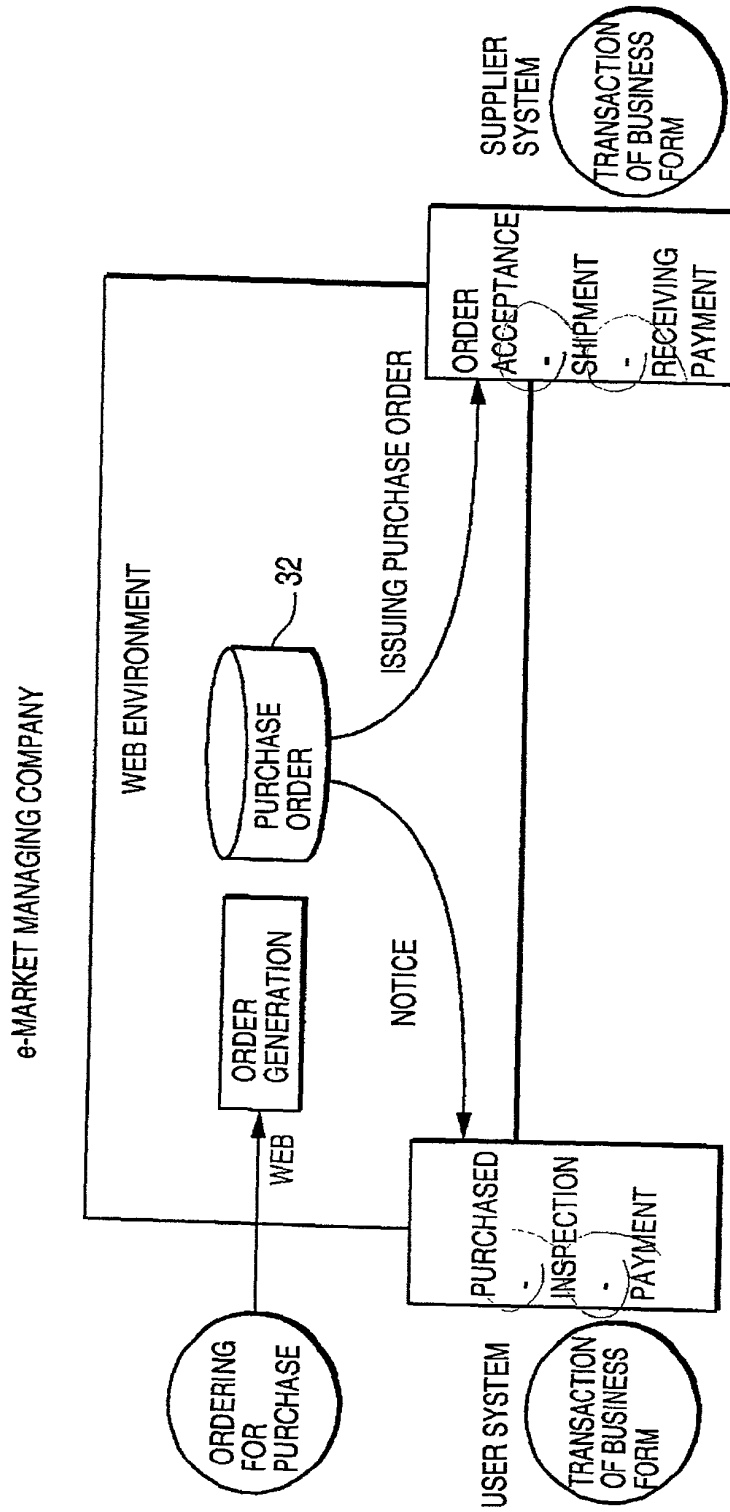
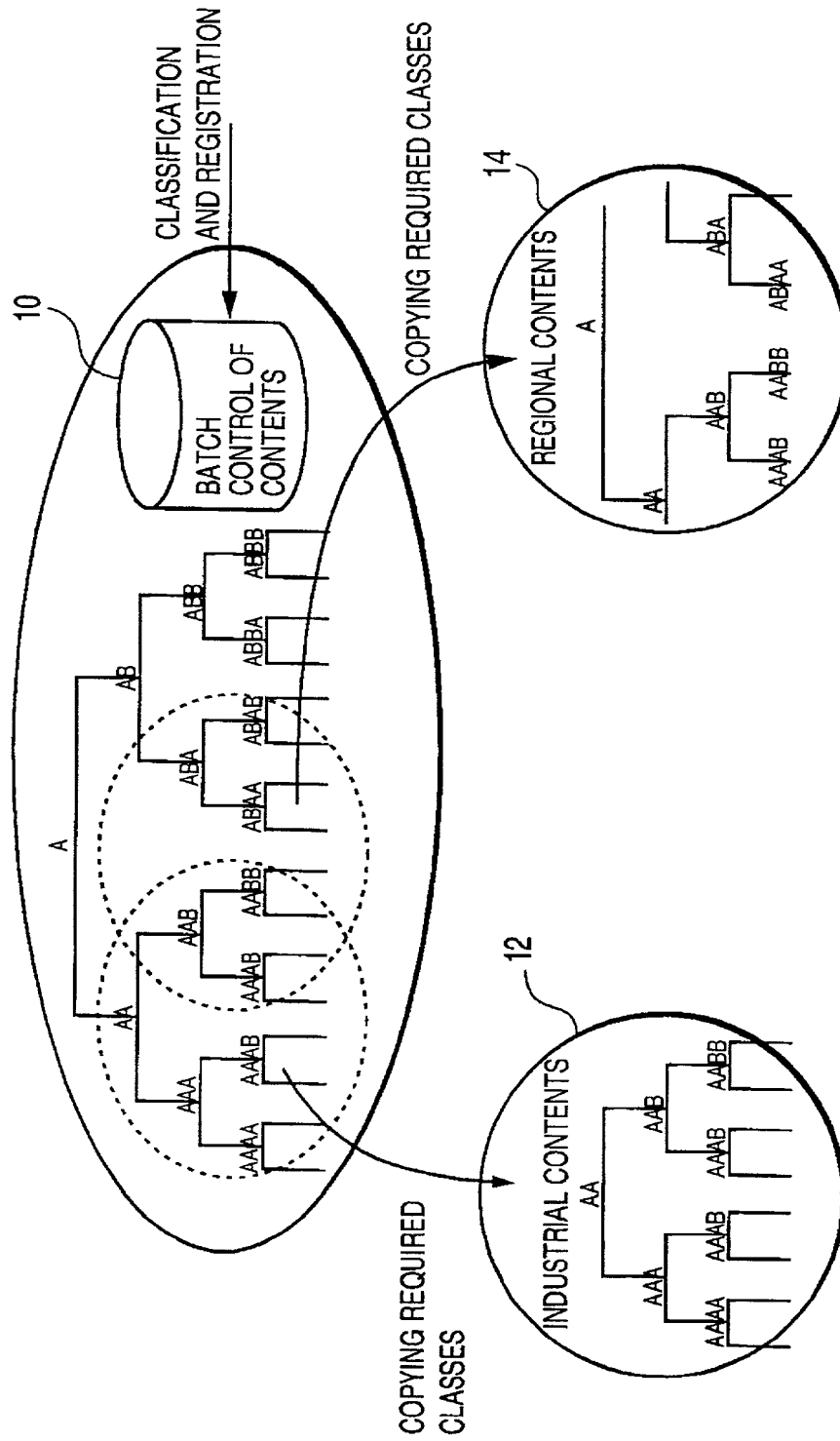


FIG. 11



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FIG. 12



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FIG. 13

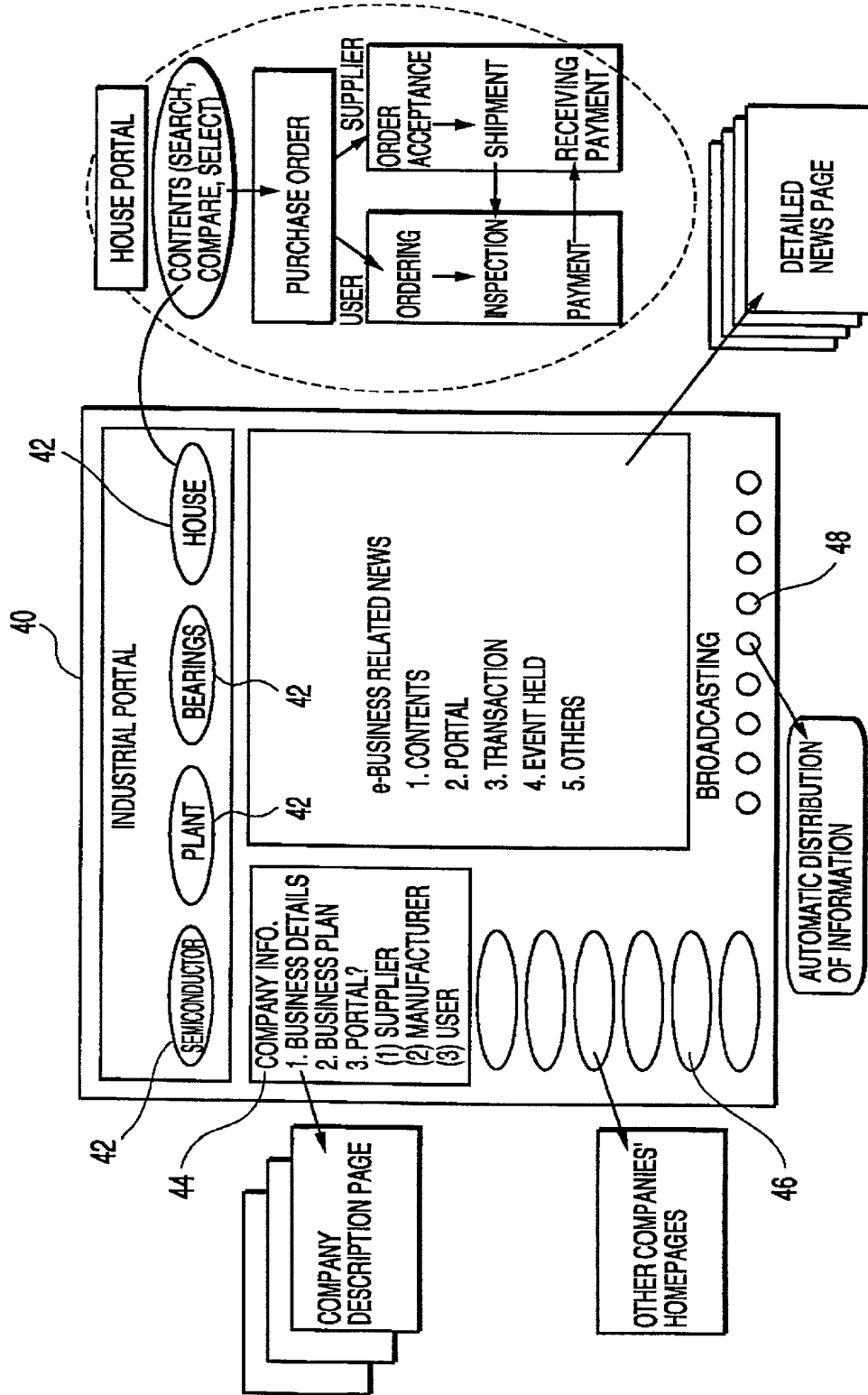
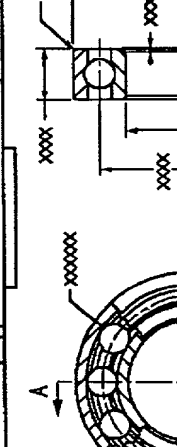


FIG. 14

☐ Comparison Memory Controller—VIP

Memory Controller—VIP	<input checked="" type="checkbox"/> Item 19	<input type="checkbox"/> Item 20	<input type="checkbox"/> Item 21
Memory Controller Type	DRAM CONTROLLER	CACHE CONTROLLER	FFD CONTROLLER
External Bus Structure	NONE	NONE	NONE
Address Bus Width	18	32	16
External Data Bus Width	0	0	0
Clock Freq—Max(fclk)(MHz)	8	20	20
Parity Generator Checker	NO	NO	NO
Error Correction	NO	NO	NO
Cascadability	NO	NO	NO
Burst Transfer Mode	NO	YES	NO
Snooping		NO	
On Chip Prog Delay Line	NO		
Memory Organization	2M x 8	128K x 8	
Number of Banks	2		
Propagation delay—Max(tpd)(ns)		50	

50



SECTION A-A

Compare

Reset

Cancel

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FIG. 15

